

## Refine Search

### Search Results -

Terms	Documents
(processor same crossbar\$1 same switch same group) and (memory same crossbar\$1 same switch same group)	43

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L5

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Wednesday, December 31, 2003   [Printable Copy](#)   [Create Case](#)

Set  
Name   Query  
 side by  
 side

Hit  
Count   Set  
                     Name  
                     result set

*DB=USPT; PLUR=YES; OP=OR*

<u>L5</u>	(processor same crossbar\$1 same switch same group) and (memory same crossbar\$1 same switch same group)	43	<u>L5</u>
<u>L4</u>	(processor same crossbar\$1 same switch) same (memory same crossbar\$1 same switch)	342	<u>L4</u>
<u>L3</u>	(processor same crossbar\$1 same switch) and (memory same crossbar\$1 same switch)	366	<u>L3</u>
<u>L2</u>	(processor same crossbar\$1) same (memory same crossbar\$1)	642	<u>L2</u>
<u>L1</u>	(processor same crossbar\$1) and (memory same crossbar\$1)	687	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

---

### Search Results -

Terms	Documents
L5	0

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L6






---

### Search History

---

DATE: Wednesday, December 31, 2003    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L6</u> <u>L5</u>	<i>DB=USPT; PLUR=YES; OP=OR</i>	0	<u>L6</u>
<u>L5</u>	(processor same crossbar\$1 same switch same group) and (memory same crossbar\$1 same switch same group)	43	<u>L5</u>
<u>L4</u>	(processor same crossbar\$1 same switch) same (memory same crossbar\$1 same switch)	342	<u>L4</u>
<u>L3</u>	(processor same crossbar\$1 same switch) and (memory same crossbar\$1 same switch)	366	<u>L3</u>
<u>L2</u>	(processor same crossbar\$1) same (memory same crossbar\$1)	642	<u>L2</u>
<u>L1</u>	(processor same crossbar\$1) and (memory same crossbar\$1)	687	<u>L1</u>

END OF SEARCH HISTORY